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now U.S. Patent
NO. 6,275,911
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Application no. 08/934,220 filed September 19, 1997, ~~still pending~~, the entire

content of which is hereby incorporated by reference in this application.--

Please replace the paragraph beginning at page 10, line 14 with the

following rewritten paragraph:

--As shown in FIG. 2, a starting address of the copy destination of the

write-control program in the microprocessor 8 on the ECU 2 side (that is, an

address where storage of the write-control program in the RAM 22 on the ECU

2 side is to start), and similarly, an end address of the copy destination of the

write-control program, a starting address of the write destination of the write

data in the microprocessor 8 on the ECU 2 side (that is, an address where

writing of the write data to the flash ROM 20a on the ECU 2 side is to start),

and similarly, an end address of the write destination of the write data.

Additionally, as illustrated in FIG. 2, information and the like relating to the

communication protocol (communication speed, communication format, and so

on) of data communication between the memory-writing device 4 and the ECU

2 also is stored in the parameter storage region M2 as other control

parameters.--

Please replace the paragraph beginning at page 52, line 10 with the

following rewritten paragraph:

--Meanwhile, moreover, the ECU 2 according to this embodiment is

structured so that the baud rate of data communication performed with the

memory-rewriting device 4 is a value which is variable in accordance with a